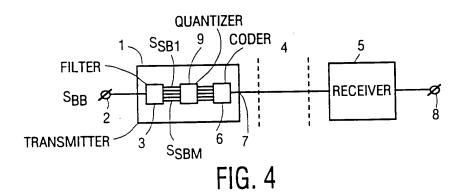


442

	FD1 ;	FD2	1	FD3		
	SYNC AND SYSTEM INFO	ALLOCATION INFO	SCALE FACTORS	SAMPLES		DUMMY SLOT
15			רור ס		IP P'	IP P'+1
			riu. Z			

b ₀ , SYNC. SIGNAL	b ₁₅ b ₁₆	NMBR SLOTS	b ₂₃ b ₂₄ FRAME FORMAT INFO b ₃₁

FIG. 3



32-

BR	Fs	В
bitrate (kbits/s)	sample freq. (kHz)	# slots in a frame
128	32 44.1 48	48 34 + padding 32
192	32 44.1 48	72 52 + padding 48
256	32 44.1 48	96 69 + padding 64
384	32 44.1 48	144 104 + padding 96

FIG. 5

bitrate (kbits/s)	total # frames in padding sequence	# frames with a dummy slot
128	147	122
192	49	12
256	147	97
384	49	24

F16.6

Bit 24 :	Frame type	0 format A 1 format B	
Bits 25 and 26:	Copyright Mode indication	0 1 no copyr 1 0 copyrigh	right, own rec. right, software nt, own recording nt, software
0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 1 0 0 1 0 0 0 1 1 0 0 1 1 0 1 0 0 0 1 0 1 0 1 0 1	0 Stereo 1 Stereo 0 Stereo 1 Stereo 1 Stereo 0 Stereo 1 Stereo 1 Stereo 1 Stereo 1 Stereo 1 Stereo 1 Stereo 2 Chan. 1 2 Chan. 1 2 Chan. 0 2 Chan.	48 kHz 44.1 kHz 44.1 kHz 32 kHz	Emphasis no emphasis 50/15 µsec
0 1 1 0 0 1 1 1 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 1 1 0 1 0 1 0 1 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 1	0 1 1 1 Chan. 1 Chan. 1 Chan. 1 1 Chan. 2 Chan. 1 Chan. 2 Chan. 1 Chan. 2 Chan	reserved 48 kHz 48 kHz 48 kHz 44.1 kHz 44.1 kHz 32 kHz 32 kHz 32 kHz	no emphasis 50/15 µsec no emphasis 50/15 µsec no emphasis 50/15 µsec no emphasis 50/15 µsec CCITT J.17

F1G. 7

mode	channel	I	channel II
stereo 2 channel mono 1 channel mono	left program program		right program II not used

```
length of samples
allocation
                 in bits
info
                          (no samples or scale factors transferred)
0000
0001
                 3
0010
0011
0100
0101
                 7
0110
                 8
0111
                 9
10
1000
1001
                 11
1010
1011
                 12
                 13
1100
                 14
1101
1110
                 not used to prevent incorrect sync detection
1111
```

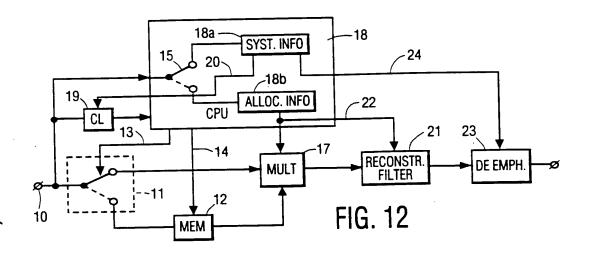
FIG.9

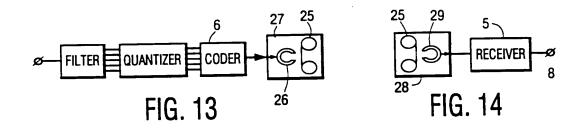
slot 2: I-1 II-1	I-2	II-2	1-3	II-3	I-4	II-4
slot 3: I-5 II-5	I-6	II-6	I-7	II-7	I-8	II-8
slot 4: I-9 II-9	I-10	II-10	. I-11	II-11	I-12	II-12
slot 5: I-13 II-13	I-14	II-14	I-15	II-15	I-16	II-16
slot 6: I-17 II-17	I-18	II-18	I-19	II-19	I-20	II-20
slot 7: I-21 II-21	I-22	II-22	I-23	II-23	I-24	II-24
slot 8: I-25 II-25	I-26	II-26	I-27	II-27	I-28	II-28
slot 9: I-29 II-29	I-30	II-30	I-31	11-31	1-32	II-32

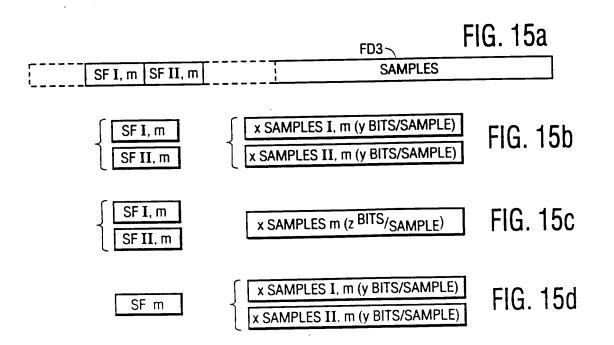
	1-2	II-2	I-3	II-3	I-4	II-4
slot 3: I-5 II-5	I-6	II-6	I-7	II-7	I-8	II-8
<pre>slot 4: I-9</pre>	1-10	II-10	I-11	11-11	I-12	II-12
I-13 II-13	I-14	II-14	I-15	II-15	I-16	II-16

FIG. 11









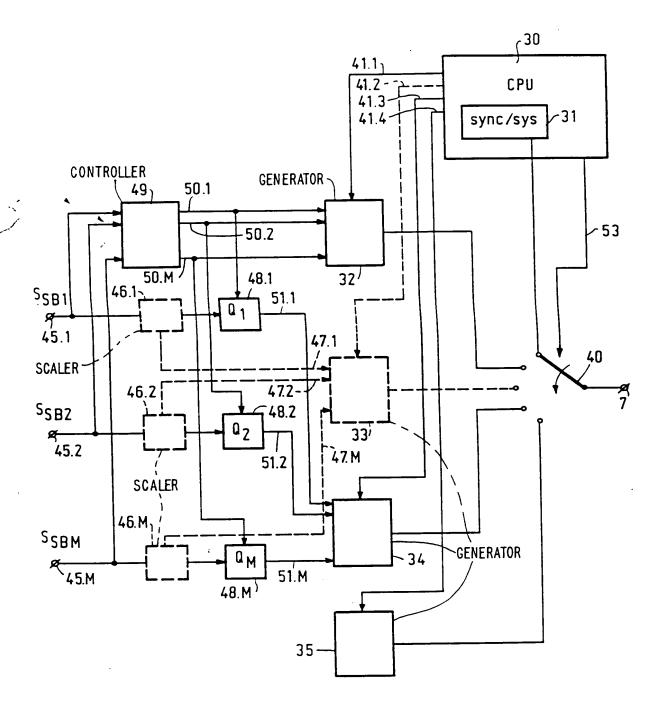


FIG.16

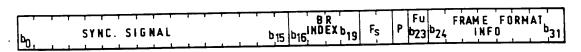


FIG.17

Bits 16 to 19 : bitrate index

BR	BR
rate	inde

bitrate (kbits/ sec.)	index	sample frequence 48 kHz # slots	cy Fs 44.1 kHz # slots	32 kHz # slots
32 64 96 128 160 192 224 256 288 320 352 384 416 448	1 2 3 4 5 6 7 8 9 10 11 12 13	8 16 24 32 40 48 56 64 72 80 88 96 104	8 17 26 34 43 52 60 69 78 87 95 104 113 121 + paddi	12 24 36 48 60 72 84 96 108 120 132 144 156 168

Bits 20 and 21 : Sample Frequency

44.1 kHz kHz 0 1 48 1 0 32 kHz reserved 1 1

Bit 22 : padding bit

'l' if the frame contains a 'dummy' slot, Otherwise '0'

Bit 23 : Future Use

reserved for future use, '0' for the time being.



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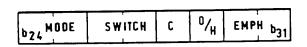


FIG.19

Bits 24 and 25 : Mode indication

0 0 stereo
0 1 intensity stereo
1 0 bilingual
1 1 mono

Bits 26 and 27 : Intensity stereo mode switches :

o o subbands 5-32 in intensity stereo mode o 1 subbands 9-32 in intensity stereo mode 1 subbands 13-32 in intensity stereo mode 1 subbands 17-32 in intensity stereo mode

no copyright 0 Bit 28 : Copyright copyright protected Bit 29 : Original/Home Copy 0 Copy Original 0 0 no emphasis Bits 30 and 31 : Emphasis $50/15~\mu sec~emphasis$ 0 1 reserved 1 0 CCITT J.17

FIG. 20

Mono mode :

M = mono signal

slot 2: M-8M-7M-1M-2M-4 M-5 M-6M-3slot 3: M - 16M-13M - 14M - 15M-9 M-10 M-11 M - 12slot 4: M = 2 hM - 23M - 20M-21M - 22M-18 M - 19M - 17slot 5: M - 32M - 31M-28 M - 29M - 30M - 25M - 26M-27

Intensity Stereo mode:

```
L = left channel, R = right channel, M = mono signal
Switch bits (bits 26 and 27) are 0 0:
slot 2:
                                       L-4
                            L-3 R-3
                                             R-4
                L-2
                       R-2
         R-1
slot 3:
                                  M-10 M-11 M-12
                M-7
                       M-8
                            M-9
     M-5
          M-6
slot 4:
                       M-16 M-17 M-18 M-19 M-20
          M-14 M-15
     M - 13
slot 5:
                       M-24 M-25 M-26 M-27
                                             M-28
                M-23
     M-21
          M-22
slot 6:
                       M - 32
    M-29 M-30 M-31
                                             FIG. 22a
Switch bits are 0 1:
slot 2:
                                  R-3 L-4
                                             R-4
                           L-3
                       R-2
                L-2
           R-1
     L-1
slot 3:
                                  R-7
                                      L-8
                                              R-8
                       R-6 L-7
           R-5
                L-6
slot 4:
                                  M-14 M-15
                       M-12 M-13
                                              M - 16
           M-10 M-11
     M-9
slot 5:
                                  M-22
                       M-20 M-21
                                              M - 24
           M-18 M-19
     M-17
slot 6:
                                  M-30
                             M-29
                                             M-32
                       M-28
                M-27
     M-25
           M-26
                                            FIG. 22b
Switch bits are 1 0 :
slot 2:
                                              R-H
                                       L-4
                            L-3
                                  R-3
                L-2
                       R-2
     L-1
           R-1
slot 3:
                                              R-8
                                  R-7 L-8
                       R-6
                            L-7
           R-5
                L-6
     L-5
slot 4:
                 L-10 R-10 L-11 R-11 L-12 R-12
     L-9
           R-9
slot 5:
                                  M-18 M-19
                                              M = 20
                       M-16 M-17
     M-13 M-14 M-15
slot 6:
                                              M - 28
                                  M-26
                                       M-27
                            M-25
                       M-24
          M-22 M-23
     M-21
slot 7:
                       M - 32
                                             FIG. 22c
     M-29 :M-30 M-31
Switch bits are 1 1:
slot 2:
                                R-3 L-4 R-4
                      R-2
                            L-3
                L-2
     L-1
          R-1
slot 3:
                                  R-7 L-8 R-8
                L-6
                      R-6
     L-5
          R-5
slot 4:
                                      L-12 R-12
                      R-10 L-11 R-11
              L-10
          R-9
     L-9
slot 5:
                      R-14 L-15 R-15 L-16
                                            R-16
                L-14
     L-13
          R-13
slot 6:
                                             M - 24
                       M-20 M-21 M-22 M-23
     M-17
          M-18 M-19
                                             m-32 FIG.22d
slot 7:
                       M-28 M-29 M-30 M-31
     M-25 M-26 M-27
```

